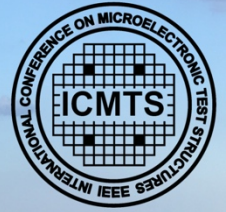




THE UNIVERSITY
of EDINBURGH

CALL FOR PAPERS ICMTS 2020



33rd International Conference on Microelectronic Test Structures April 6-9, 2020, Edinburgh, Scotland

Looking for the best opportunity to present and discuss your ideas and results about test structures, measurements and characterisation? This is your chance! [Join the 33rd ICMTS conference.](#)

A **Tutorial Short Course** will precede the main conference. Several of the best measurement, equipment design, and manufacturing experts, will participate in the **equipment exhibition** and presentations.

The conference will bring together designers and users of test structures to discuss recent developments and future directions, in a **one track program**, with convivial breaks allowing attendees to discuss and exchange viewpoints and challenges.

A **Best Paper award** will be presented by the Technical Program Committee.

The conference is co-sponsored by the **IEEE Electron Devices Society** and all accepted papers, if presented, will be submitted for possible inclusion on IEEEXplore®.

Original papers are solicited presenting new developments in topics relevant to ICMTS, including but not limited to, test structures, measurements, and results, in the following areas:

- **Measurement techniques**
 - ▶ DC, AC and RF measurements: setup, test and analysis
 - ▶ Reliability test - including thermal stability, failure analysis etc.
 - ▶ Statistical analysis, variability, throughput increase, smart test strategies
 - ▶ Use of machine learning and AI in analysis of data sets - parameter extraction etc.
- **Device test**
 - ▶ Emerging memory technologies (single cell, arrays, and application in neural networks)
 - ▶ Photonic devices - silicon integration, new displays (OLED, μ -displays)
 - ▶ Flexible electronics and sensors (organic and inorganic materials)
 - ▶ M(N)EMS, actuators, sensors, PV cells and other emerging devices
 - ▶ Yield enhancement and process control tests
- **Test structures for material characterisation**
- **Device and circuit modelling**

The author's abstract submission consists of up to four pages in PDF format (font-embedded). The first page should include a **title**, a **50-word summary**, author name(s), full address, contact number, and e-mail of the lead author, and any preference for oral or poster session presentation. The body of the abstract should consist of **one page of text** (800 to 1000 words) and **up to two pages of major figures and tables**.

The selection process will be based on the technical merit and will be highly weighted in favour of abstracts with high test structure content, giving a clear illustration of the test structure and including measurements and data analysis.

The abstract submission deadline is October 20, 2019.

Abstracts can be submitted via the ICMTS 2020 website www.icmts.net using the "Abstract Submission" link the front page.

Notice of paper acceptance will be sent to the selected authors by **early December, 2019**, with instructions for the expanded manuscript preparation for the conference proceedings. The deadline for submission of the final paper will be **February 2, 2020**.

You may care to join the ICMTS group at www.linkedin.com.

Details of the venue, hotel, registration, etc. will be posted when available at the ICMTS 2020 official web site:

For further technical information, please contact the technical program chair:

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